

2122  
en



SN 09/938,886

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Mukesh K Patel	Examiner:	Chameli C. Das
Serial No.:	09/938,886	Group Art Unit:	2122
Filed:	August 24, 2001	Docket:	000006.P001C
Title:	Java Hardware Accelerator Using Microcode Engine		

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the referenced materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicants have included the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p). Please charge any additional fees or credit any overpayment to Deposit Account No. 503437.

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

In accordance with the Patent and Trademark Office's waiver of the requirement under 37 C.F.R. § 1.98(a)(2)(i), copies of the listed U.S. patents are not enclosed because the current application has been filed after June 30, 2003.

05/10/2005 RFEKADU1 00000016 09938886  
01 FC:1806 180.00 OP

Respectfully submitted,

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Serial No 09/938,886

Filing Date: August 24, 2001

Title: Java Hardware Accelerator Using Microcode Engine

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Page 2

Dkt: 000006.P001C

MUKESH K PATEL

By his Representatives,

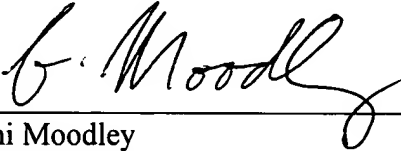
Hahn and Moodley LLP

Suite 180

CA 94040

Date May 6, 2005

By



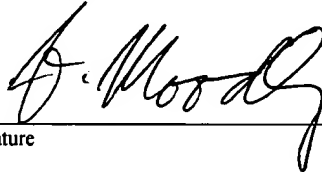
Vani Moodley

Reg. No. 56631

**CERTIFICATE UNDER 37 CFR 1.8:** The undersigned hereby certifies that the correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313 1450, on this 6th day of May, 2005.

VANI MOODLEY

Name



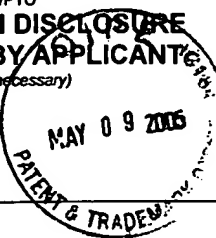
Signature

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number	09/938,886
Filing Date	August 24, 2001
First Named Inventor	Patel, Mukesh
Group Art Unit	2122
Examiner Name	Das, Chameli

Sheet 1 of 1

Attorney Docket No: 000006.P001C

**US PATENT DOCUMENTS**

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	US-6,606,743	08/12/2003	Raz, Yair, et al.	

**FOREIGN PATENT DOCUMENTS**

Examiner Initials *	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>2</sup>

**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>

**EXAMINER****DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional) <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Mukesh K Patel  
Title: Java Hardware Accelerator Using Microcode Engine

Docket No.: 000006.P001C  
Filed: August 24, 2001  
Examiner: Chameli C. Das

Serial No.: 09/938,886  
Due Date: N/A  
Group Art Unit: 2122

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard and this transmittal document.
- ☒ An Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.). Documents NOT enclosed, cited in parent application.
- ☒ A check in the amount of \$180 to cover the fee for consideration of Information Disclosure Statement under 97(c).

If not provided for in a separate paper filed herewith,

HAHN AND MOODLEY LLP  
Customer Number: 52418

By

Vani Moodley  
Reg. No. 56631  
VM:CMG:vm

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 6th day of May, 2005.

VANI MOODLEY

Name

Signature

(GENERAL)